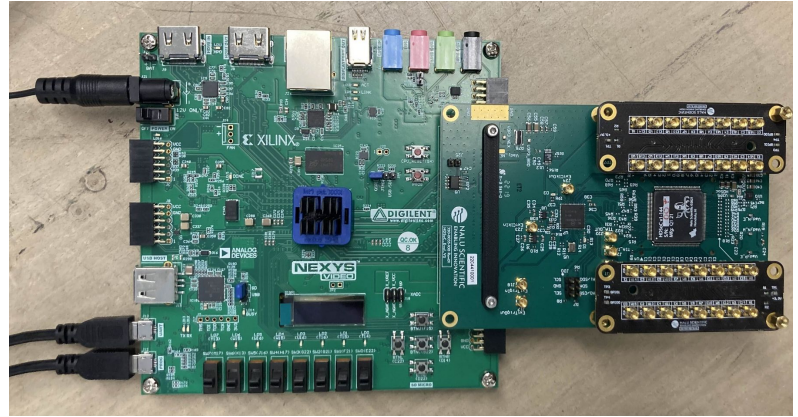
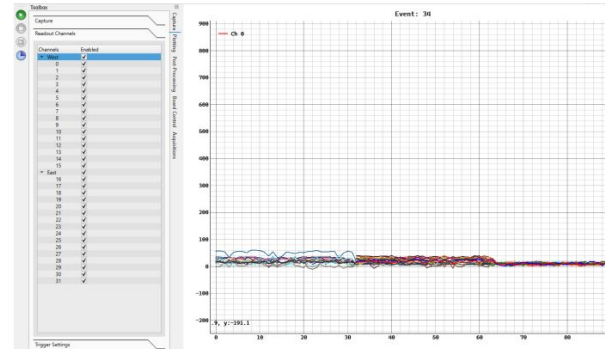


ATAR Teststand DAQ (Naludaq)

- Want to use Nalu's Python library for readout integration into a python midas frontend
 - Load appropriate firmware
 - Run naluscope
 - Connect to board in python
 - Configure board in python
 - Readout data in python
 - UART
 - 1GbE(?)
 - Integrate into midas python frontend
- Questions for Nalu about adapting their examples



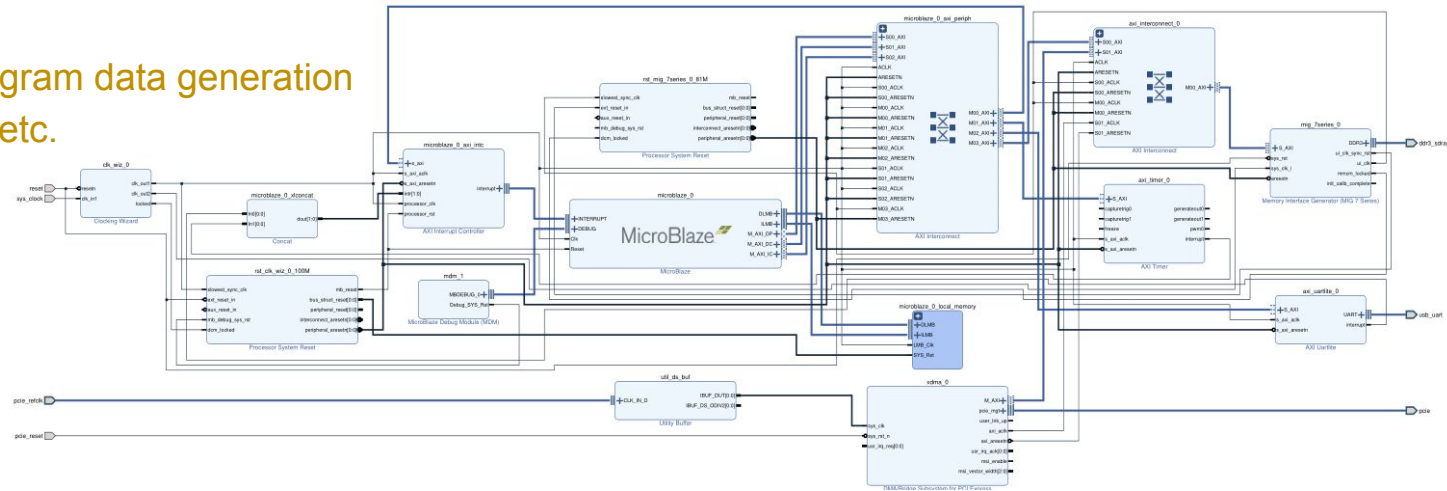
Nexys A7 Video Board with Nalu's HDSoc Digitizer Attached as an FMC Module



NaluScope Program Screenshot with Noise Traces

PIONEER DAQ: PCIe DMA Firmware Additions

- Added MicroBlaze IP Block
 - Readout DDR3 registers over PCIe via DMA
 - Run C++ code natively on the the FPGA
 - Want to program data generation simulators, etc.



Block diagram for PCIe DMA transfer with microblaze added